

International Journal of Engineering Research and Applications (IJERA)

CERTIFICATE

It is certify that the paper entitled by "Memory Controller for DDR SDRAM and It's Verification Using Verilog For DSP Processors" has been published in International Journal of Engineering Research and Applications (IJERA).

Your article has been published with following details:

Author's Name:	P. Chenchu Navya Lalithya
Journal Name:	International Journal of Engineering Research and Applications (IJERA)
Journal Web:	www.ijera.com
Journal Type:	Online & Print (Both)
Review Type:	Peer Review Refereed
ISSN:	2248-9622
Vol No.:	13
Issue No.:	10
Publication Month:	October
Publication Year:	2023



Editor-In-Chief International Journal of Engineering Research and Applications (IJERA) E-mail ID: ijera.editor@gmail.com Web: www.ijera.com



International Journal of Engineering Research and Applications (IJERA)

CERTIFICATE

It is certify that the paper entitled by "Memory Controller for DDR SDRAM and It's Verification Using Verilog For DSP Processors" has been published in International Journal of Engineering Research and Applications (IJERA).

Your article has been published with following details:

Author's Name:	K.Dhanumjaya
Journal Name:	International Journal of Engineering Research and Applications (IJERA)
Journal Web:	www.ijera.com
Journal Type:	Online & Print (Both)
Review Type:	Peer Review Refereed
ISSN:	2248-9622
Vol No.:	13
Issue No.:	10
Publication Month:	October
Publication Year:	2023



Editor-In-Chief International Journal of Engineering Research and Applications (IJERA) E-mail ID: ijera.editor@gmail.com Web: www.ijera.com